

Attorney Docket Number | 001701.00193

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Sheet 2

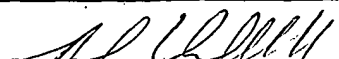
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Complete if Known

Application Number	TBA; Continuation of SN 10/315,030
Filing Date	herewith
First Named Inventor	Tomoharu Tanaka
Group Art Unit	2824 (parent)
Examiner Name	Son T. Dinh (parent)
Attorney Docket Number	001701.00193

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
SN		Tae-Sung JUNG et al.; "A3.3V 128 Mb Multi-Level NAND Flash Memory for Mass Storage Applications," ISSCC Digest of Technical papers, pp. 32-33, Feb. 1996	
SN		Tae-Sung JUNG et al.; "A 117-mm ² 3.3-V 128-Mb Multilevel NAND Flash Memory For Mass Storage Applications," IEEE Journal of Solid-State Circuits, Vol. 31, No. 11, November 1996, pp. 1575-1583	
SN		Young, Joon Choi et al.; "A High Speed Programming Scheme For Multi-Level NAND Flash Memory," Samsung Electronics Co., Ltd., Kiheung, Korea, 1996 Symposium on VLSI Circuits Digest of Technical Papers, pp. 170-171	

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